

FIG. 1

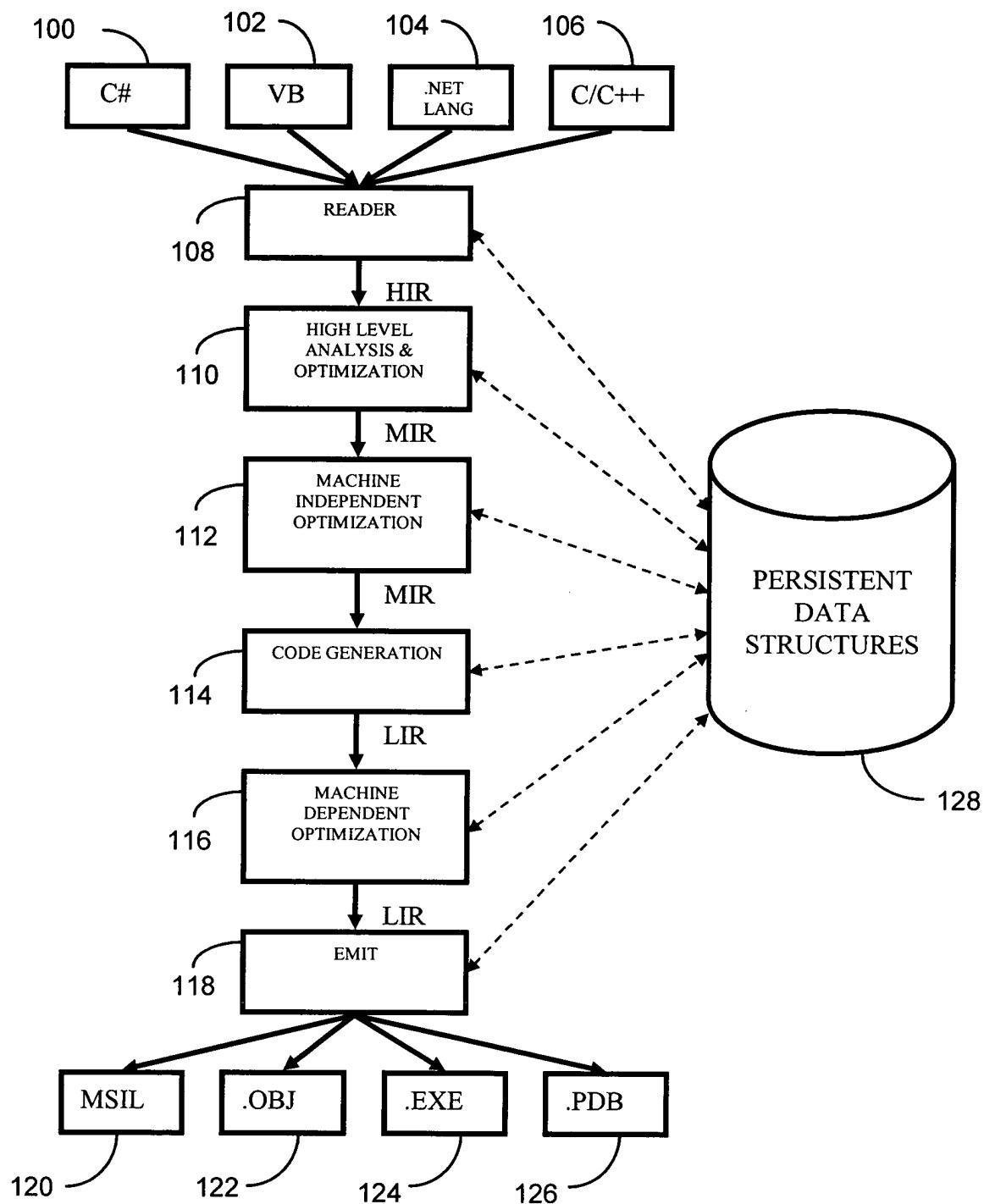


FIG. 2

Source:

```
z = a + b + c; - 200
```

HIR:

```
t1 = ADD a, b - 202  
t2 = ADD t1, c - 204  
z = ASSIGN t2 - 206
```

LIR:

```
t1 (EAX) , cc = x86add a (EAX) , b (EDX) - 208  
t2 (EAX) , cc = x86add t1 (EAX) , c (EBX) - 210  
z = x86mov t2 (eax) - 212
```

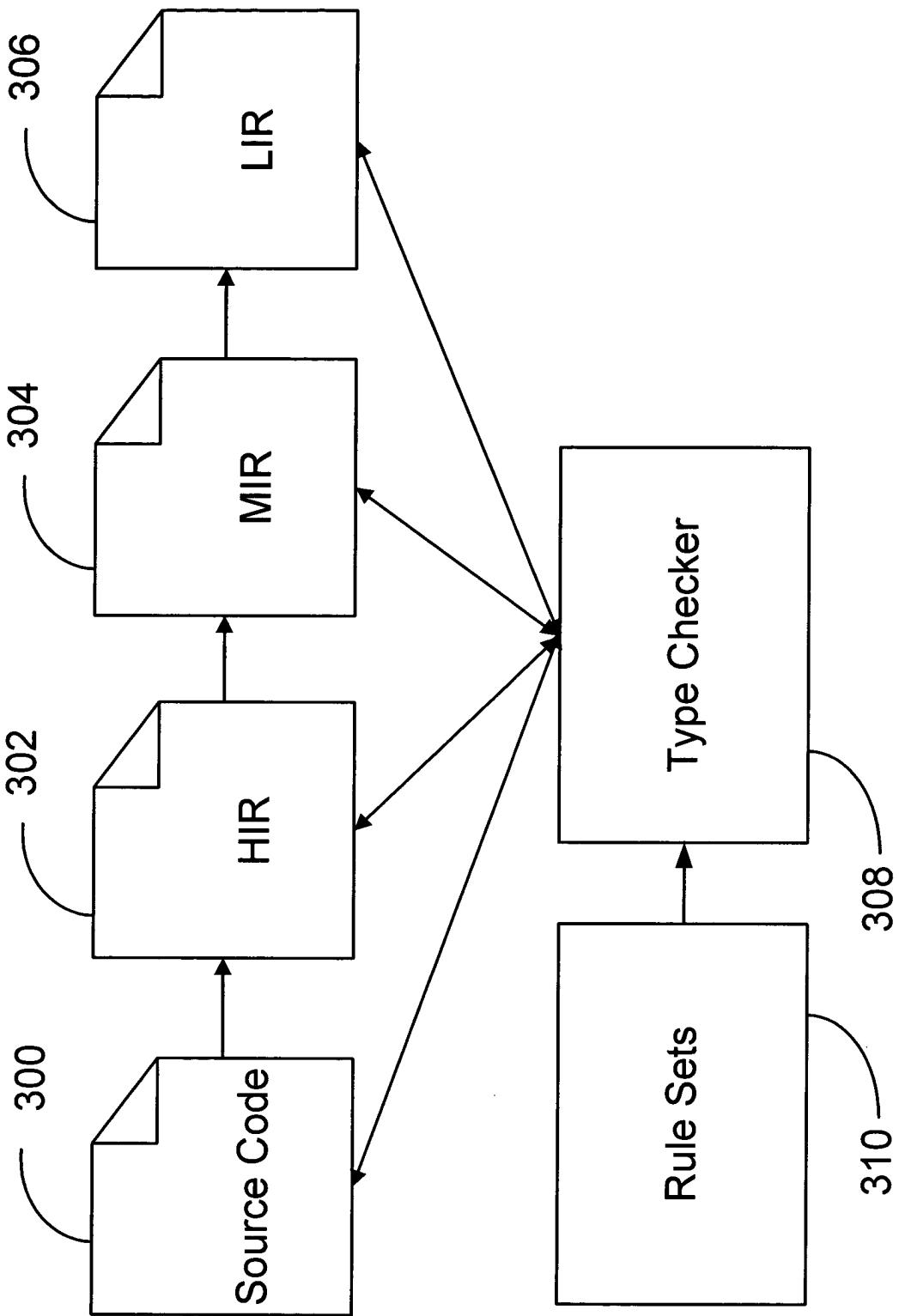
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Inventors: Plesko and Tarditi
Express Mail No. EV351283278US
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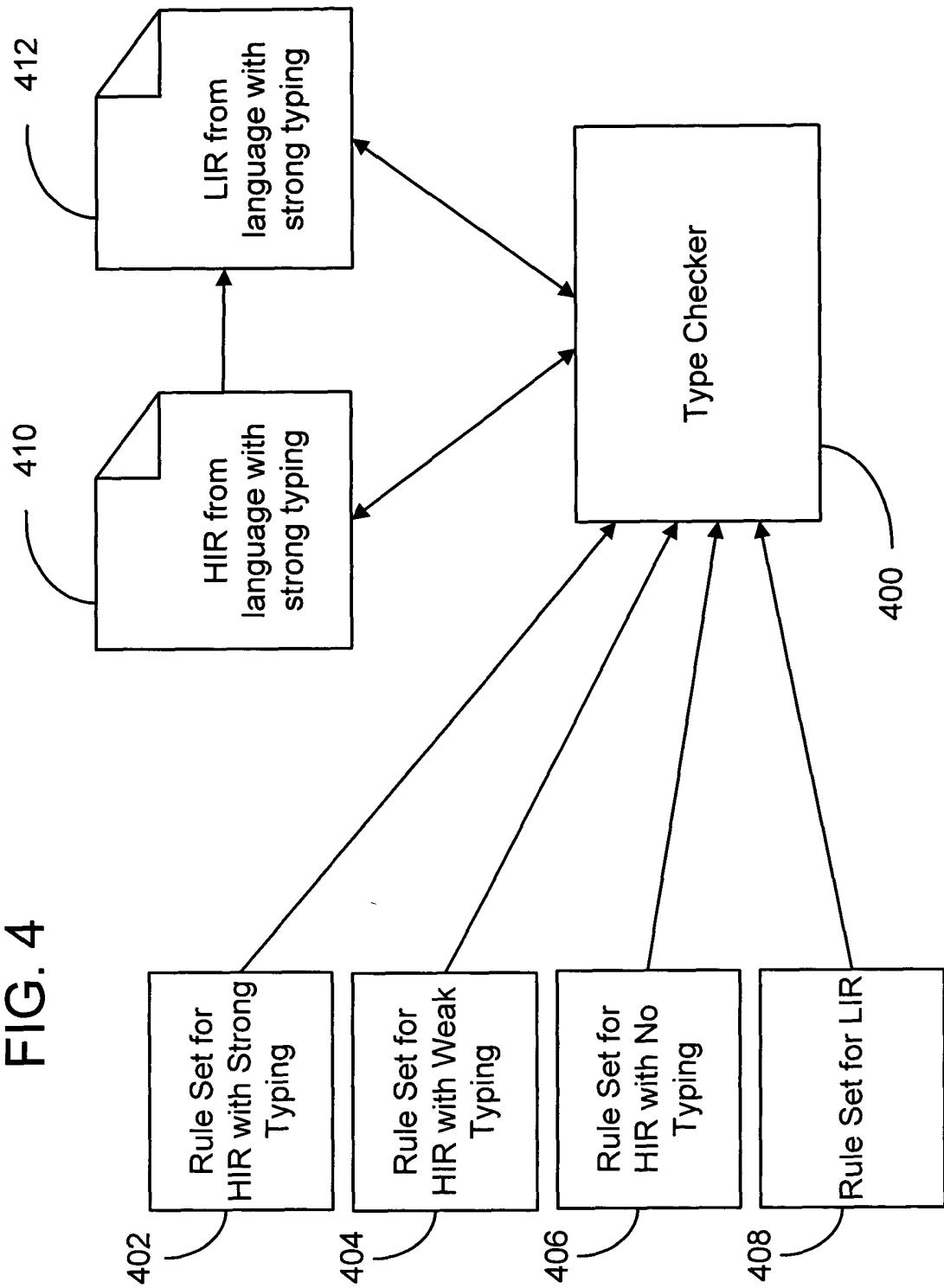
FIG. 3



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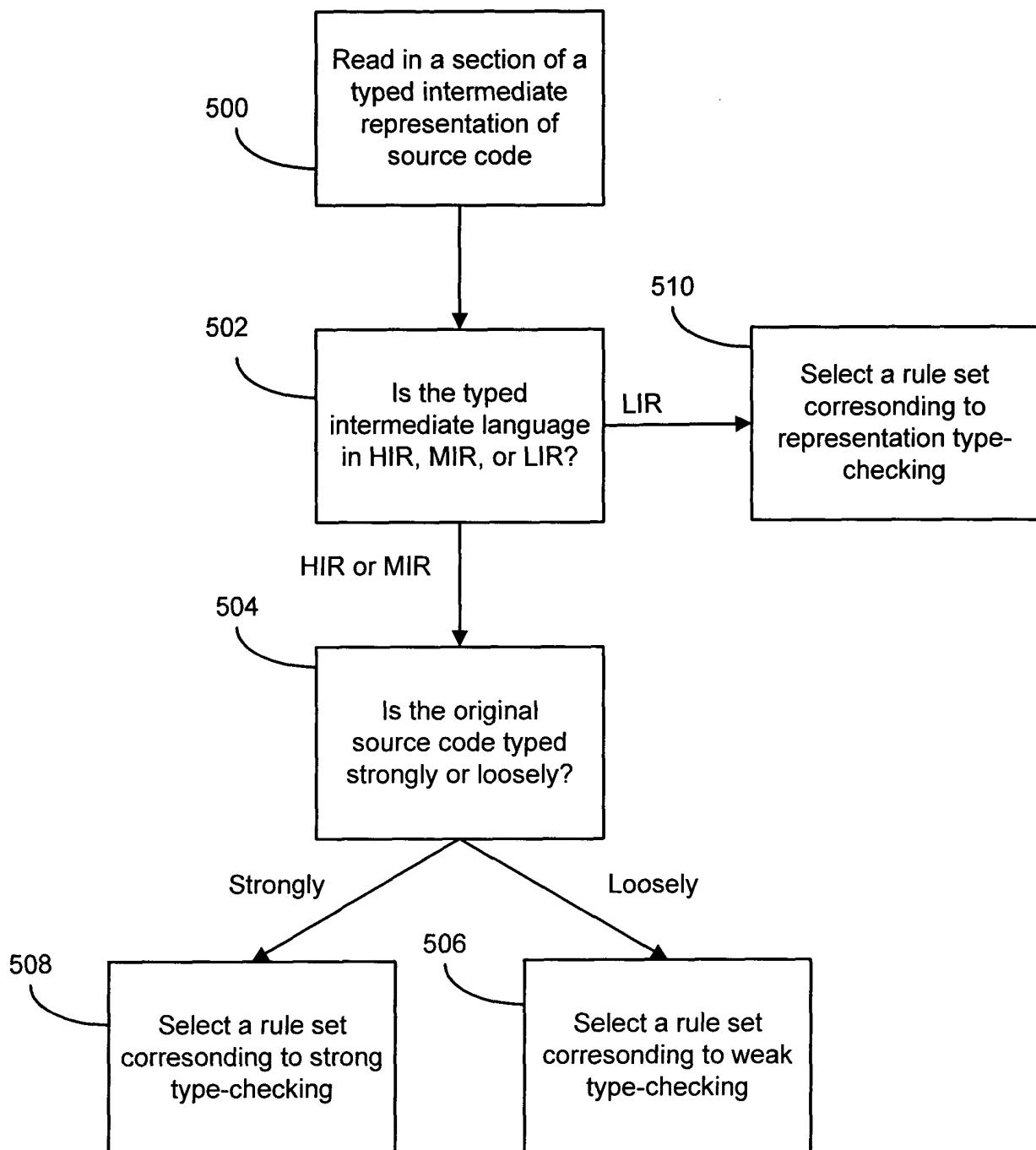
FIG. 4



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FIG. 5



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FIG. 6

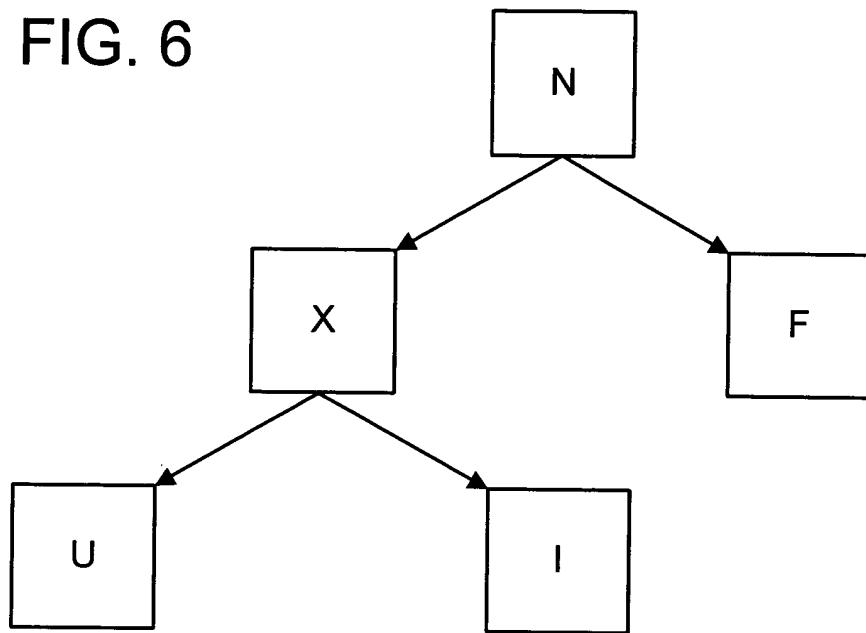
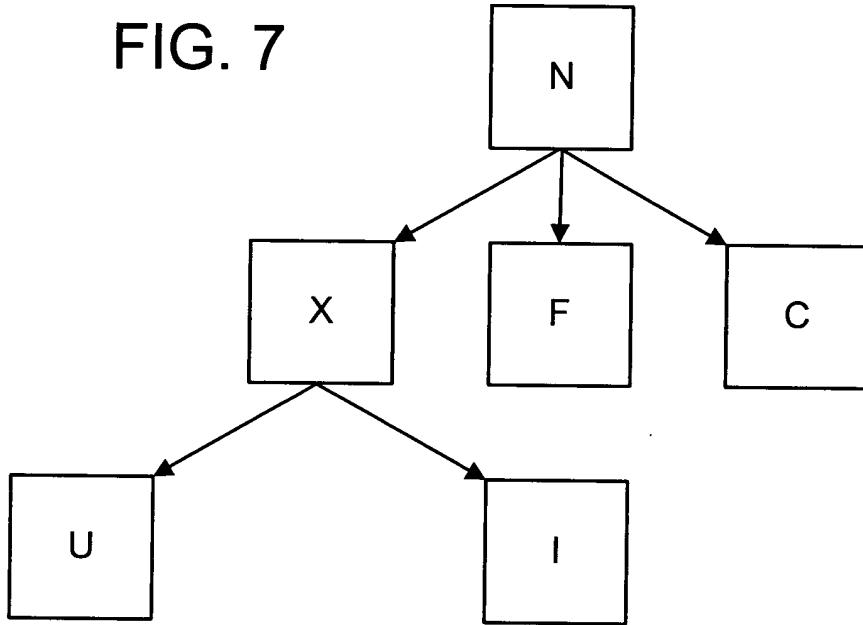


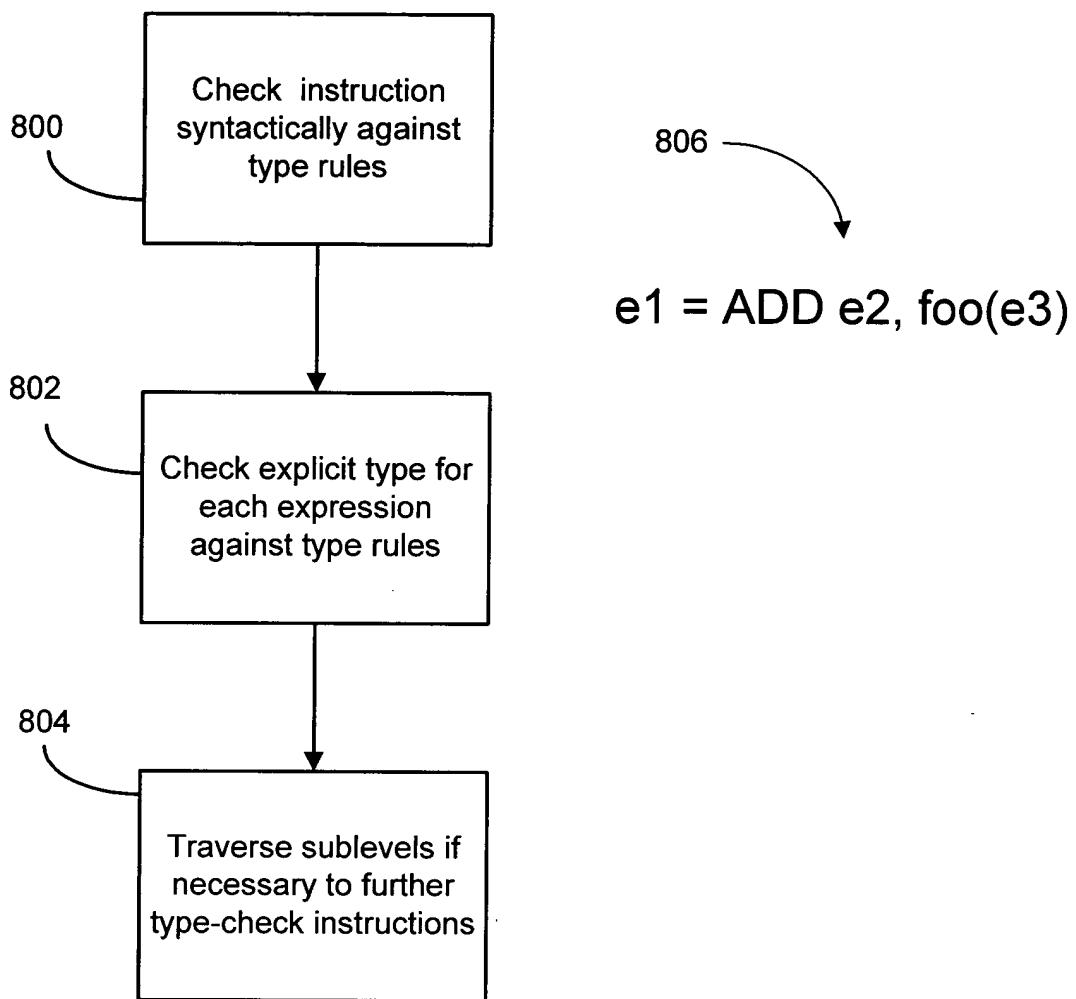
FIG. 7



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FIG. 8



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